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APPLICATION NO.	PLICATION NO. FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO. 5042	
10/615,787	15,787 07/10/2003		Hirotaka Oosawa	HITA.0413		
38327	7590	09/22/2004		EXAMINER		
REED SMI	TH LLP		NGUYEN, JIMMY			
3110 FAIRV	IEW PAF	RK DRIVE, SUITE 1				
FALLS CHURCH, VA 22042				ART UNIT	PAPER NUMBER	
				2829		

DATE MAILED: 09/22/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	No.	Applicant(s)			
		10/615,787		OOSAWA ET AL.			
Of	fice Action Summary	Examiner	Art Unit				
		Jimmy Nguy		2829			
The l Period for Repl	MAILING DATE of this communication app Y	ears on the co	over sheet with the co	orrespondence ad	dress		
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).  Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
Status							
·	nsive to communication(s) filed on <u>10 Ju</u>						
′=	(a) This action is <b>FINAL</b> . 2b) ⊠ This action is non-final.						
-	this application is in condition for allowar	•			e merits is		
ciosec	I in accordance with the practice under E	x parte Quay	e, 1935 C.D. 11, 45	3 O.G. 213.			
Disposition of	Claims						
<ul> <li>4)  Claim(s) 11 - 19 is/are pending in the application.</li> <li>4a) Of the above claim(s) is/are withdrawn from consideration.</li> <li>5)  Claim(s) is/are allowed.</li> <li>6)  Claim(s) 11, 15 - 17 and 19 is/are rejected.</li> <li>7)  Claim(s) 12 - 14 and 18 is/are objected to.</li> <li>8)  Claim(s) are subject to restriction and/or election requirement.</li> </ul>							
Application Pa	pers						
9) ☐ The specification is objected to by the Examiner.  10) ☑ The drawing(s) filed on 10 July 2003 is/are: a) ☑ accepted or b) ☐ objected to by the Examiner.  Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority under	35 U.S.C. § 119						
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>							
Attachment(s)							
2) Notice of Dra 3) Information D	erences Cited (PTO-892) ftsperson's Patent Drawing Review (PTO-948) isclosure Statement(s) (PTO-1449 or PTO/SB/08) Mail Date	(PTO-413) ite atent Application (PTC	O-152)				

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#### **DETAILED ACTION**

#### Claims status

The applicants acknowledges claims 1 – 10 have been canceled in the preliminary amendment dated 7/10/2003, therefore, claims 11 – 19 are pending in the application.

### **Double Patenting**

1. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970);and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

Claims 11, 15 – 17 and 19 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-10 of U.S.
 Application Patent # 6597191. Although the conflicting claims are not identical, they are not patentably distinct from each other.

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#### Claims matching

US Application 10/615787	US Patent 6597191		
11	1		
15, 17, 19	8		
16	10		

It would have been obvious to a person of ordinary skill in the art at the time of the invention was made to use "a first wiring layer" and "a second wiring layer" to replace "wiring connection" for the purpose distinct the connection.

## Allowable Subject Matter

1. Claims 12 – 14 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The prior arts of record are silent on the combination of the claim 1 and the digital circuit has a first voltage terminal for receiving the voltage and the analog circuit has a second voltage terminal for receiving the voltage, wherein the voltage providing means includes a first switching circuit coupled between the first wiring layer and the first voltage terminal and a second switching circuit coupled between the first wiring layer and the second voltage terminal, and wherein the connection node between the second

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switching circuit and the first wiring layer is located between the selection circuit and the

connection node between the first switching circuit and the first wiring layer.

2. Claim 18 is objected to as being dependent upon a rejected base claim, but

would be allowable if rewritten in independent form including all of the limitations of the

base claim and any intervening claims.

The prior arts of record are silent on the combination of the claim 16 and the

voltage providing circuit includes a third switching circuit coupled between the wiring

layer and the voltage to be supplied to the transmitting circuit and a fourth switching

circuit coupled between the wiring layer and the voltage to be supplied to the test circuit,

and wherein the fourth switching circuit is located between the third switching circuit and

the test circuit.

Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Jimmy Nguyen at (703) 306-5858. Any inquiry of

a general nature of relating to the status of this application or proceeding should

be directed to the Group receptionist whose telephone number is (703) 305 - 4900.

JN.

Sep 16, 2004

PRIMARY EXAMINER

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